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FIG. 1

CS <sub>i</sub>	CS <sub>i</sub> -> CS <sub>i+1</sub>	CS <sub>i</sub> -> CS <sub>i-1</sub>
CS1	AV_RXQUAL_LT < CS_QUAL_UL_1.2	Impossible
CS2	AV_RXQUAL_LT < CS_QUAL_UL_2.3	AV_RXQUAL_LT > CS_QUAL_UL_1.2 + CS_HST_UL_LT or AV_RXQUAL_ST > CS_QUAL_UL_1.2 + CS_HST_UL_ST
CS3	AV_RXQUAL_LT < CS_QUAL_UL_3.4	AV_RXQUAL_LT > CS_QUAL_UL_2.3 + CS_HST_UL_LT or AV_RXQUAL_ST > CS_QUAL_UL_2.3 + CS_HST_UL_ST
CS4	Impossible	AV_RXQUAL_LT > CS_QUAL_UL_3.4 + CS_HST_UL_LT or AV_RXQUAL_ST > CS_QUAL_UL_3.4 + CS_HST_UL_ST

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**FIG-2**

CSi	CSi → CSi+1	CSi → CSi-1
CS1	AV_RXQUAL_LT < CS_QUAL_DL_1_2	Impossible
CS2	AV_RXQUAL_LT < CS_QUAL_DL_2_3	AV_RXQUAL_LT > CS_QUAL_DL_1_2 + CS_HST_DL_LT OR AV_RXQUAL_ST > CS_QUAL_DL_1_2 + CS_HST_DL_ST
CS3	AV_RXQUAL_LT < CS_QUAL_DL_3_4 AV_SIR > CS_SIR_DL_3_4	AV_RXQUAL_LT > CS_QUAL_DL_2_3 + CS_HST_DL_LT OR AV_RXQUAL_ST > CS_QUAL_DL_2_3 + CS_HST_DL_ST
CS4	Impossible	AV_SIR < CS_SIR_DL_3_4 + CS_SIR_HST_DL